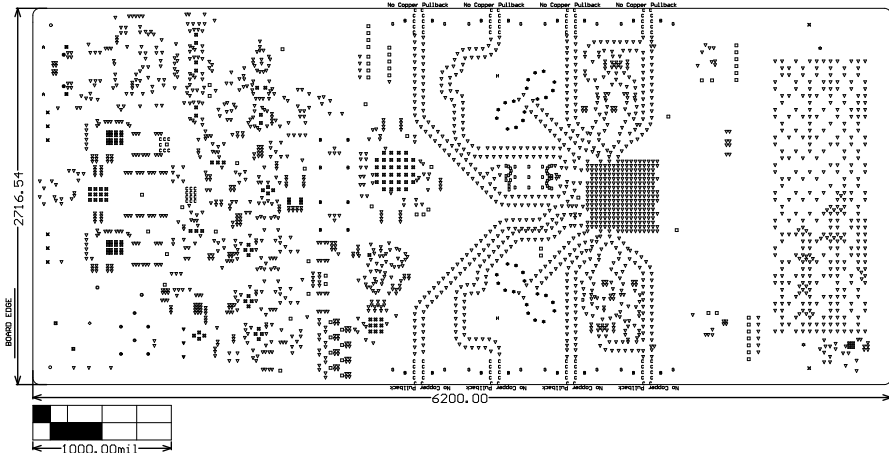


[illegible]

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



ALL artwork viewed from top side	BOARD #: DC363	REV: E1	SUN REV: c341f7e8e71ea01591b3856f1f9309178df4763 [Locally Modified]
LAYER NAME = HALO Board Dimensions			
PLOT NAME = Fabrication Drawing	GENERATED : 1/28/2025 6:17:37 AM		TEXAS INSTRUMENTS

Layer	Name	Material	Thickness	Constant	Board Layer	Stack
1	Top Overlay	Solder Resist	0.50mil	3.5		
	Top Solder					
	Top Layer					
2	Dielectric 1	Megtron6	5.70mil	3.23		
	L2 GND	CF-004	1.40mil			
	Dielectric 2	Megtron6	3.03mil	3.22		
3	L3 PWR	CF-004	1.40mil			
	Dielectric 3	Megtron6	3.03mil	3.22		
	L4 GND	CF-004	1.40mil			
5	Dielectric 4	Megtron6	3.03mil	3.22		
	L5 PWR	CF-004	1.40mil			
	Dielectric 5	Megtron6	3.03mil	3.22		
6	L6 GND	CF-004	1.40mil			
	Dielectric 6	Megtron6	4.09mil	3.13		
	L7 SIG	CF-004	1.40mil			
7	Dielectric 7	Megtron6	3.86mil	3.34		
	L8 GND	CF-004	1.40mil			
	Dielectric 8	Megtron6	3.03mil	3.22		
9	L9 GND	CF-004	1.40mil			
	Dielectric 9	Megtron6	3.86mil	3.34		
	L10 SIG	CF-004	1.40mil			
11	Dielectric 10	Megtron6	4.09mil	3.13		
	L11 GND	CF-004	1.40mil			
	Dielectric 11	Megtron6	3.03mil	3.22		
12	L12 PWR	CF-004	1.40mil			
	Dielectric 12	Megtron6	3.03mil	3.22		
	L13 GND	CF-004	1.40mil			
13	Dielectric 13	Megtron6	3.03mil	3.22		
	L14 PWR	CF-004	1.40mil			
	Dielectric 14	Megtron6	3.03mil	3.22		
15	L15 GND	CF-004	1.40mil			
	Dielectric 15	Megtron6	5.70mil	3.23		
	Bottom Layer		1.40mil			
16	Board Layer Stack Bottom Solder	Solder Resist	0.50mil	3.5		
	Board Layer Stack Bottom Overlay					
Total board thickness:			77.97mil			

Total board thickness:

77.97mil

Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

FAB Notes		DESIGN INFORMATION	
MIN. TRACK WIDTH: <u>3</u> MIL MIN. CLEARANCE: <u>4</u> MIL MIN. VIA PAD SIZE: <u>18</u> MIL MINIMUM ANNULAR RING 0.05mm (2ML) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/- <u>5</u> MIL, HOLES +/- <u>3</u> MIL HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- <u>3</u> MIL			
MATERIAL: <input type="checkbox"/> HYBRID STACKUP <input checked="" type="checkbox"/> UNIFORM STACKUP			
<input type="checkbox"/> ISOLA FR4-370HR <input type="checkbox"/> Megtron 4 <input checked="" type="checkbox"/> Isola I-Speed <input checked="" type="checkbox"/> Megtron6 <input checked="" type="checkbox"/> Isola MT-40 <input checked="" type="checkbox"/> Nelco MW-1000 <input type="checkbox"/> OTHER _____			
THICKNESS: <input type="checkbox"/> 62 MIL (1.6mm) +/-10% <input checked="" type="checkbox"/> OTHER 78 MIL +/-10%			
TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____			
BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____			
DRILLING: REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC DRILL FILES PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER _____			
BOARD FINISH: SILKSCREEN: <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER _____ SOLDER RESIST COLOR: <input type="checkbox"/> GREEN <input checked="" type="checkbox"/> OTHER PURPLE <input checked="" type="checkbox"/> MATTIE <input type="checkbox"/> SEMI-GLOSS			
SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENEPIC <input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> HARD GOLD (30u) <input type="checkbox"/> OTHER _____			
ARRAY/PANEL: <input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE <input type="checkbox"/> N.C. ROUTE <input checked="" type="checkbox"/> V. SCORE			
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF: <input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3 <input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER _____ PER ORDER			
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER			
ADDITIONAL REQUIREMENTS: MICROSECTION: <input type="checkbox"/> YES BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER			
<h2 style="margin: 0;">FAB INSTRUCTIONS</h2>			
<input checked="" type="checkbox"/> 16 MIL & SMALLER VIAS REQUIRE NON-CONDUCTIVE FILL AND PLAINAZE <input checked="" type="checkbox"/> OUTER LAYER TRACKS 12:1 MIL WIDE REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE <input checked="" type="checkbox"/> TOP LAYER TRACKS 6:3 MIL WIDE WITH 4 MIL SPACE REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE <input checked="" type="checkbox"/> BOTTOM LAYER TRACKS 5:6 MIL WIDE WITH 3.5 MIL SPACE REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE <input checked="" type="checkbox"/> INNER LAYER TRACKS 3 MIL WIDE WITH 4 MIL SPACE REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE <input checked="" type="checkbox"/> NO PULL-BACK ON COPPER FEATURES TO EDGE OF THE BOARD			
<div style="display: inline-block; vertical-align: middle;"> <h2 style="margin: 0;">TEXAS INSTRUMENTS</h2> </div>			
PROJECT TITLE: DAC39RF 20EVM			
DESIGNED FOR: Public Release			
FILE NAME: DAC39RF 20EVM_PcbDoc			
ENGINEER: MK		LAYOUT BY: GR	
SCALE: 1.00		ALTIM DESIGNER VERSION: 24.10.1.45	